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TFW 2616
PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Branko D. KOVACEVIC
Title: SYSTEM FOR FRAME BASED AUDIO SYNCHRONIZATION AND METHOD THEREOF
Application No.: 09/833,493 Filed: April 11, 2001
Examiner: Daniel J. RYMAN Group Art Unit: 2616
Atty. Docket No.: ATL0100330 (1376-0100330)

MS AMENDMENT
COMMISSIONER FOR PATENTS
PO Box 1450
Alexandria, VA 22313-1450

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- ☒ Form(s) PTO/SB/08A and/or PTO/SB/08B or PTO/1449
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indicates the degree of relevance found by the foreign office.

STATEMENT UNDER 37 C.F.R. § 1.704(d)

If the above-identified application is an original application filed on or after May 29, 2000:

- ☐ each item of information contained in this Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart application and this communication was not received by any individual designated in § 1.56(c) more than thirty days prior to the filing of this Information Disclosure Statement.

FEES DUE

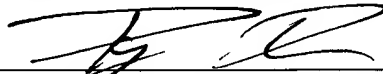
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- ☐ before the mailing date of a first Office action on the merits or before the mailing date of a first Office action after the filing of a request for continued examination under § 1.114. Therefore, no fee is believed required.
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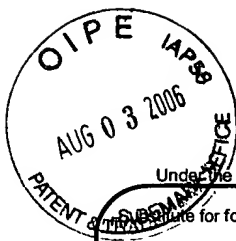
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31 July 2066
Date

Respectfully submitted,



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PTO/SB/08B (07-05)

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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

Complete if Known

Application Number	09/833,493
Filing Date	April 11, 2001
First Named Inventor	Branko D. KOVACEVIC
Art Unit	2616
Examiner Name	Daniel J. RYMAN
Attorney Docket Number	ATI.0100330 (1376-0100330)

Sheet	1	of	1
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NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	A1	SONDERMEYER, Jeff; "Rational Sample Rate Conversion with Blackfin Processors"; Engineer to Engineer Note EE-183; Analog Devices; January 20, 2003; Pages 1-25	
	B1		
	C1		
	D1		
	E1		
	F1		
	G1		
	H1		
	I1		
	J1		

Examiner Signature		Date Considered	
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